

Serial No. 10/681,280

H-626-11

REMARKS

The Applicants request reconsideration of the rejection.

Claims 32-40 are now pending.

The Examiner objected to the drawings under 37 C.F.R. §1.83(a) as failing to show the claimed latch circuits, and rejected claims 32-36 and 39 under 35 U.S.C. §112, first paragraph, for failing to provide adequate support in the specification for the latch circuits. In reply to each of these matters, the Applicants refer the Examiner to Fig. 2 and the corresponding disclosure from page 18, line 14 to page 22, line 14, and specifically to page 21, line 15 to page 22, line 14 (for example). Fig. 2 shows illustratively two sense latch circuits SLT that are coupled to corresponding bit (data) lines.

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Each aspect of the Office Action having been addressed,  
the Applicants request reconsideration of the rejection and  
allowance of the claims.

Respectfully submitted,

  
Daniel J. Stanger  
Registration No. 32,846  
Attorney for Applicants

MATTINGLY, STANGER & MALUR  
1800 Diagonal Rd., Suite 370  
Alexandria, Virginia 22314  
(703) 684-1120  
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